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Signed: _____

Kimberly Melvin

Atty. Docket No.: SANDP043
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

CHANG, Robert C. et al.

Application No.: 10/676,407

Filed: October 1, 2003

For: DYNAMIC REDUNDANT AREA CONFIGURATION IN
A NON-VOLATILE MEMORY SYSTEM

Examiner: Unassigned

Group: Unassigned

Date: October 22, 2003

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR §§1.56 AND 1.97(c)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is being filed within three (3) months of the filing date of the above-referenced application. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1652 (Order No. SANDP043).

Respectfully submitted,


Peggy A. Su
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Form 1449 (Modified)	Atty Docket No. SANDP043
Information Disclosure Statement By Applicant	Application No.: 10/676,407
	Inventor Robert C. Chang et al.
	Group Unassigned
	Filing Date October 1, 2003
	Mailing Date October 22, 2003
(Use Several Sheets if Necessary)	

U.S. Patent or Published Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub-Class	Filing Date
	A	5,222,109	6/22/93	Pricer	377	24.1	12/28/90
	B	5,297,148	3/22/94	Harari et al.	371	10.2	10/20/92
	C	5,388,083	2/7/95	Assar et al.	365	218	3/26/93
	D	5,438,573	8/1/95	Mangan et al.	371	10.3	6/1/94
	E	5,568,439	10/22/96	Harari	365	218	6/6/95
	F	5,598,370	1/28/97	Nijjima et al.	365	185.33	4/26/95
	G	5,835,935	11/10/98	Estakhri et al.	711	103	9/13/95
	H	5,845,313	12/1/98	Estakhri et al.	711	103	7/31/95
	I	5,860,082	1/12/99	Smith et al.	711	103	3/28/96
	J	5,907,856	5/25/99	Estakhri et al.	711	103	3/31/97
	K	5,924,113	7/13/99	Estakhri et al.	711	103	5/29/98
	L	6,016,275	1/18/00	Han	365	185.29	11/4/98
	M	6,081,447	6/27/00	Lofgren, et al.	365	185.02	3/5/99
	N	6,115,785	9/5/00	Estakhri et al.	711	103	5/13/99
	O	6,125,435	9/26/00	Estakhri et al.	711	201	11/24/97
	P	6,230,233	5/8/01	Lofgren, et al.	711	103	9/13/91
	Q	6,260,156	7/10/01	Garvin et al.	714	8	12/4/98
	R	6,426,893	7/30/02	Conley et al.	365	185.11	2/17/00

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub-class	Translation	
							Yes	No
	S	62-283496	12/9/87	JP			X	
	T	62-283497	12/9/87	JP			X	

Other Documents

Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
	U	Kim, Jesung et al., "A Space-Efficient Flash Translation Layer for Compactflash Systems", IEEE Transactions on Consumer Electronics, Vol. 48, No. 2, May 2002
Examiner		Date Considered

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.
Include copy of this form with next communication to applicant.